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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

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10058709	01/30/2002	257		2811	

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****CONTINUING DATA VERIFIED:**
 THIS APPLICATION IS A CON OF 09/771,670 01/30/2001 PAT 6,355,975
 WHICH IS A DIV OF 09/449,834 11/26/1999 PAT 6,342,726
 AND A CON OF 08/822,933 03/21/1997 ABN
 AND A CON OF 09/760,733 01/17/2001 PAT 6,342,728
 AND A CON OF 09/764,378 01/19/2001 PAT 6,353,255
 AND A CON OF 09/765,376 01/22/2001
 AND A DIV OF 09/768,451 01/25/2001 PAT 6,355,500
 AND A CON OF 09/768,288 01/25/2001
 AND A CON OF 09/770,493 01/29/2001 ABN
 AND A DIV OF 09/770,494 01/29/2001
 AND A CON OF 09/771,648 01/30/2001
 AND A DIV OF 09/771,985 01/30/2001

**** FOREIGN APPLICATIONS VERIFIED:**
 JAPAN 8-66637 03/22/1996

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
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Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO 501.35286C15
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no	
Verified and Acknowledged Examiners's Initials	

TITLE : Semiconductor device and manufacturing method thereof

U.S. DEPT. OF COMM. PAT. & TM. PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
ISSUE FEE Amount Due Date Paid		Assistant Examiner	Total Claims Print Claim for O.G.
		Primary Examiner	DRAWING Sheets Drwg. Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner
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